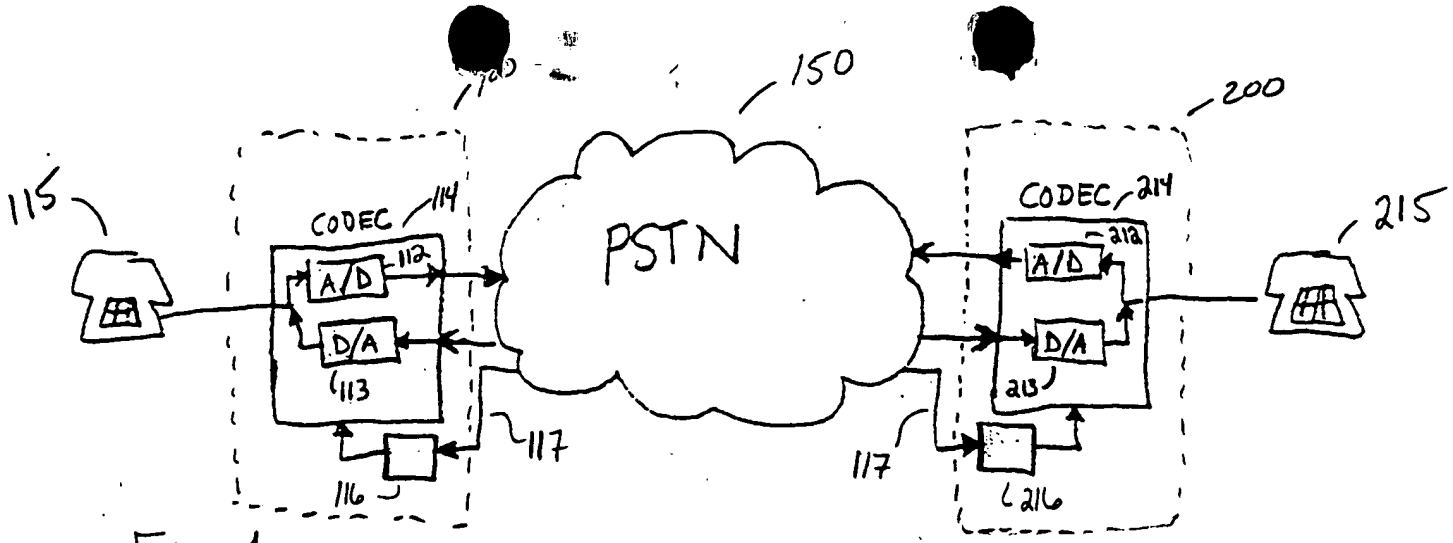

Fig. 1

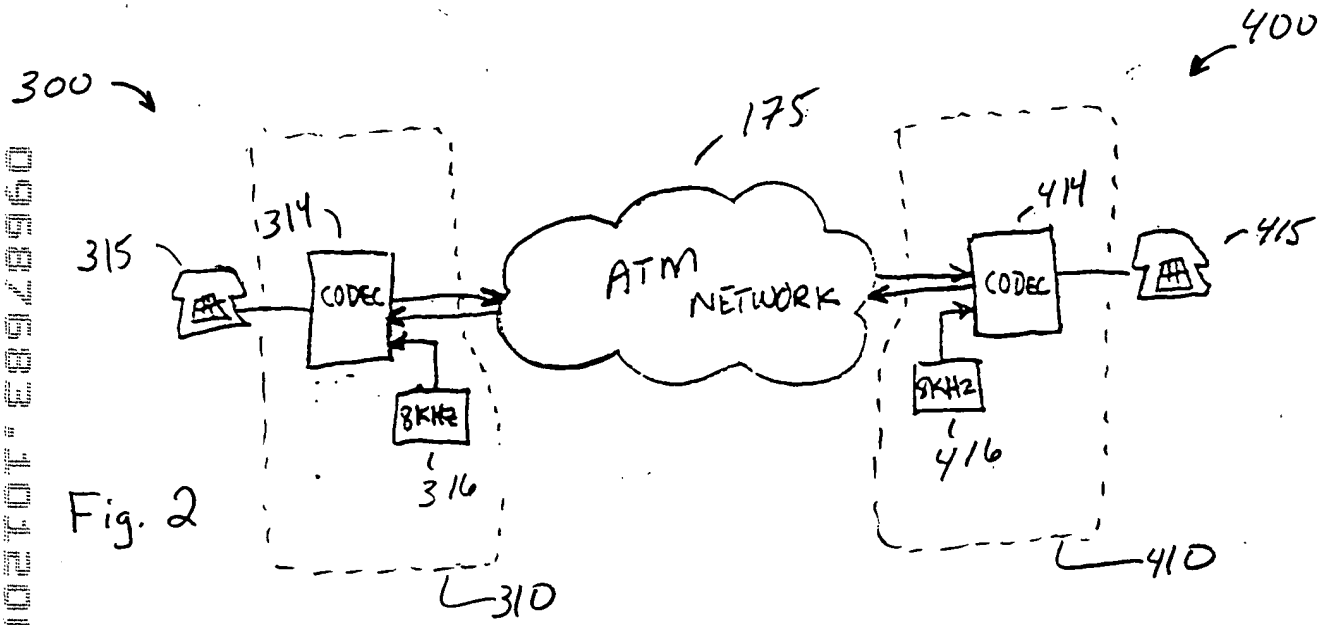

Fig. 2

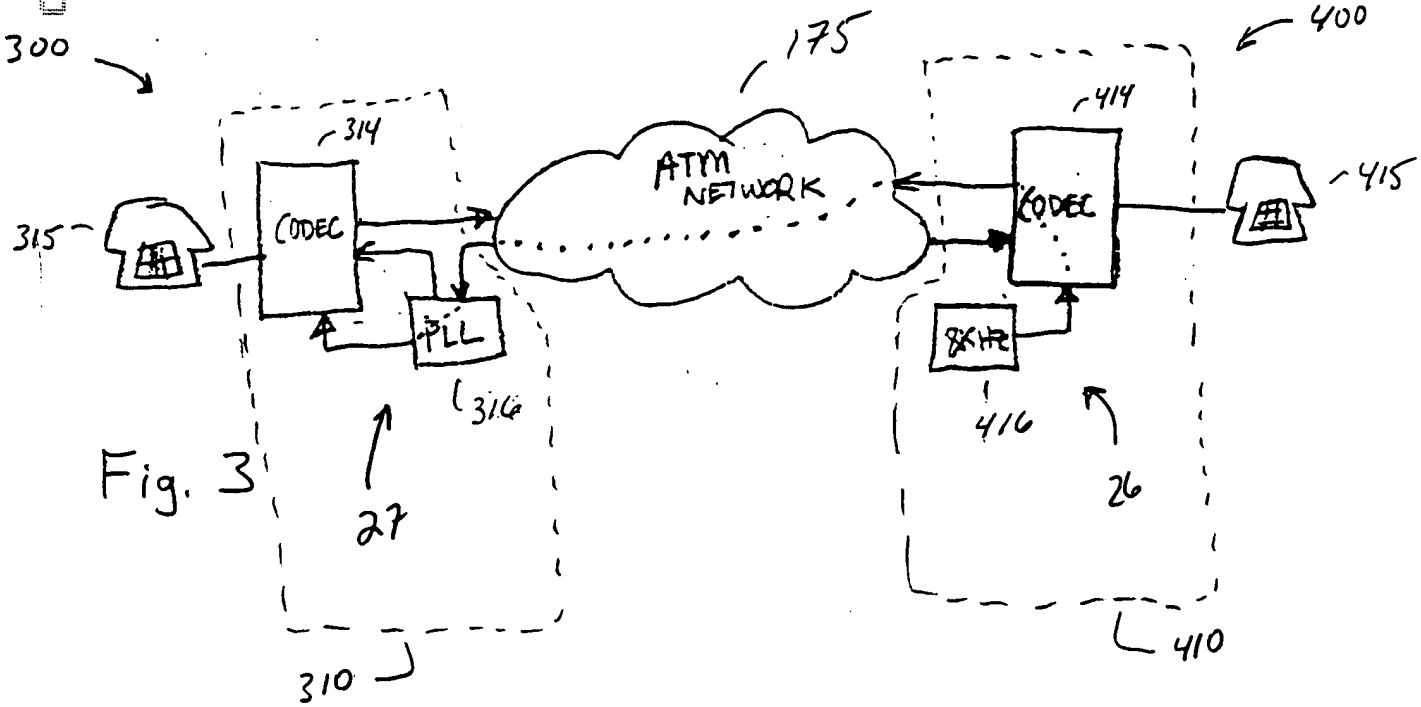

Fig. 3

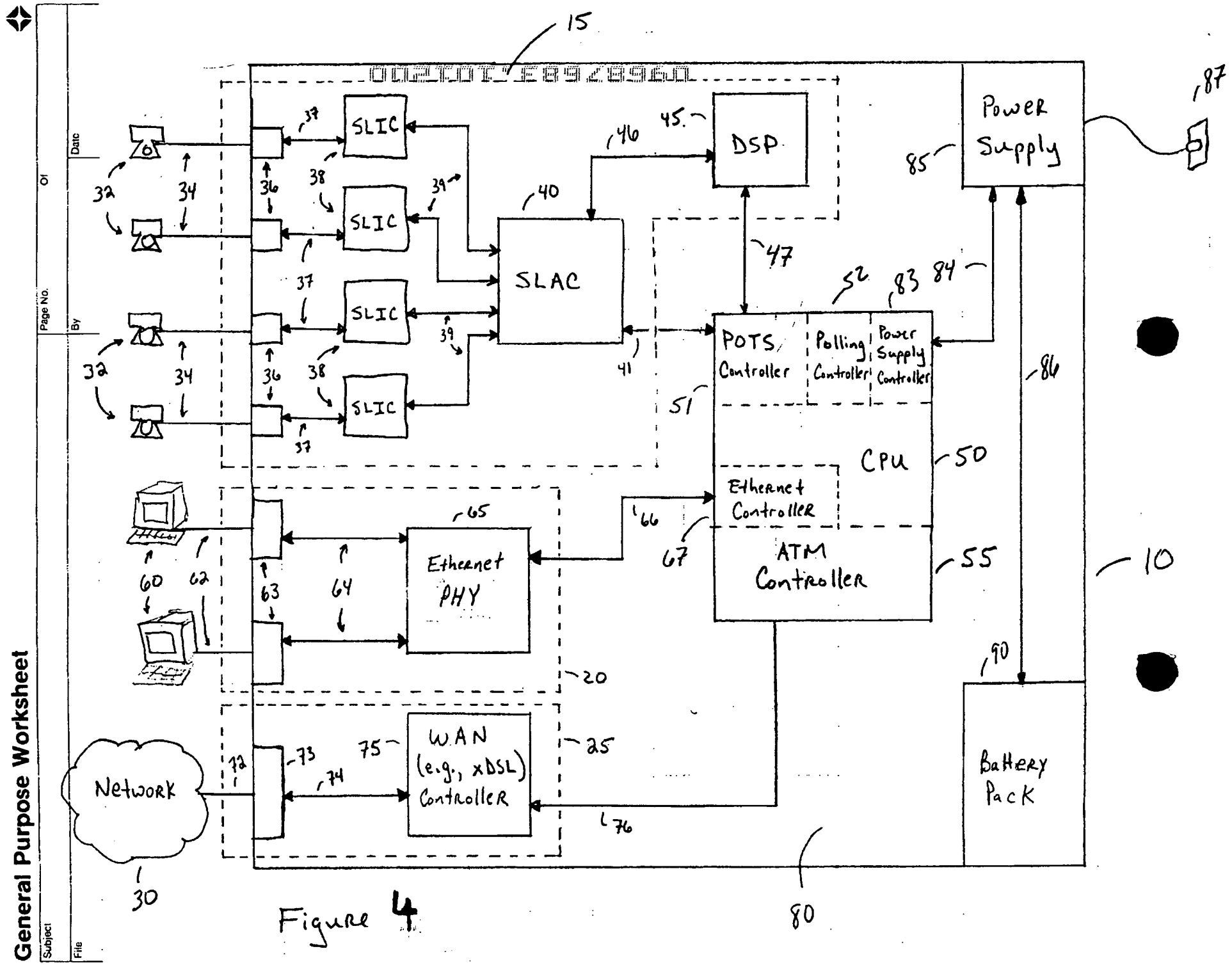
15
SLIC
SLIC
SLIC
SLIC
SLAC
DSP
45
46
47
40
41
Power Supply
85
87
POTS Controller
Polling Controller
Power Supply Controller
52
83
84
86
51
CPU
50
Ethernet Controller
ATM Controller
55
10
65
Ethernet PHY
66
67
20
WAN (e.g., xDSL) Controller
75
25
Network
30
80
Battery Pack
90
32
34
36
37
38
39
60
62
63
64
72
73
74
76

Figure 4

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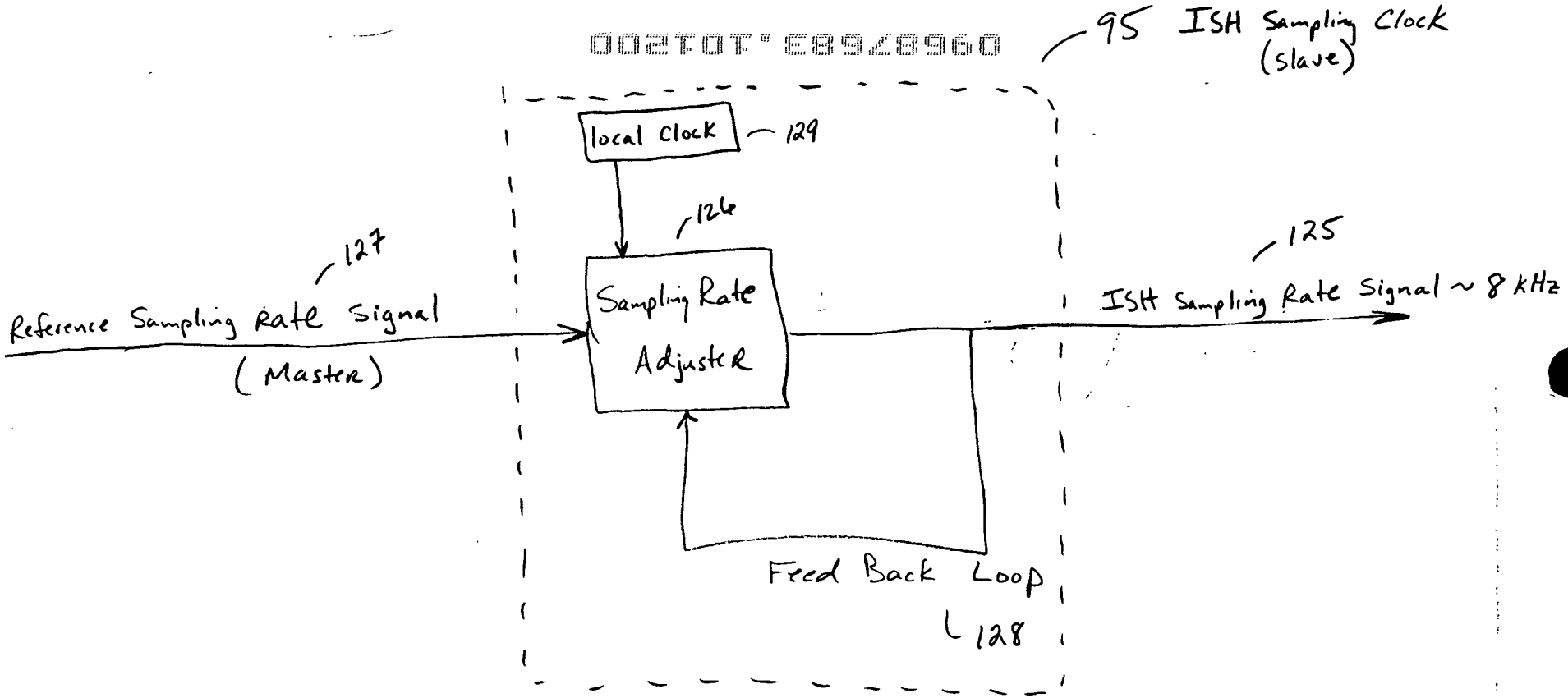

Fig. 5

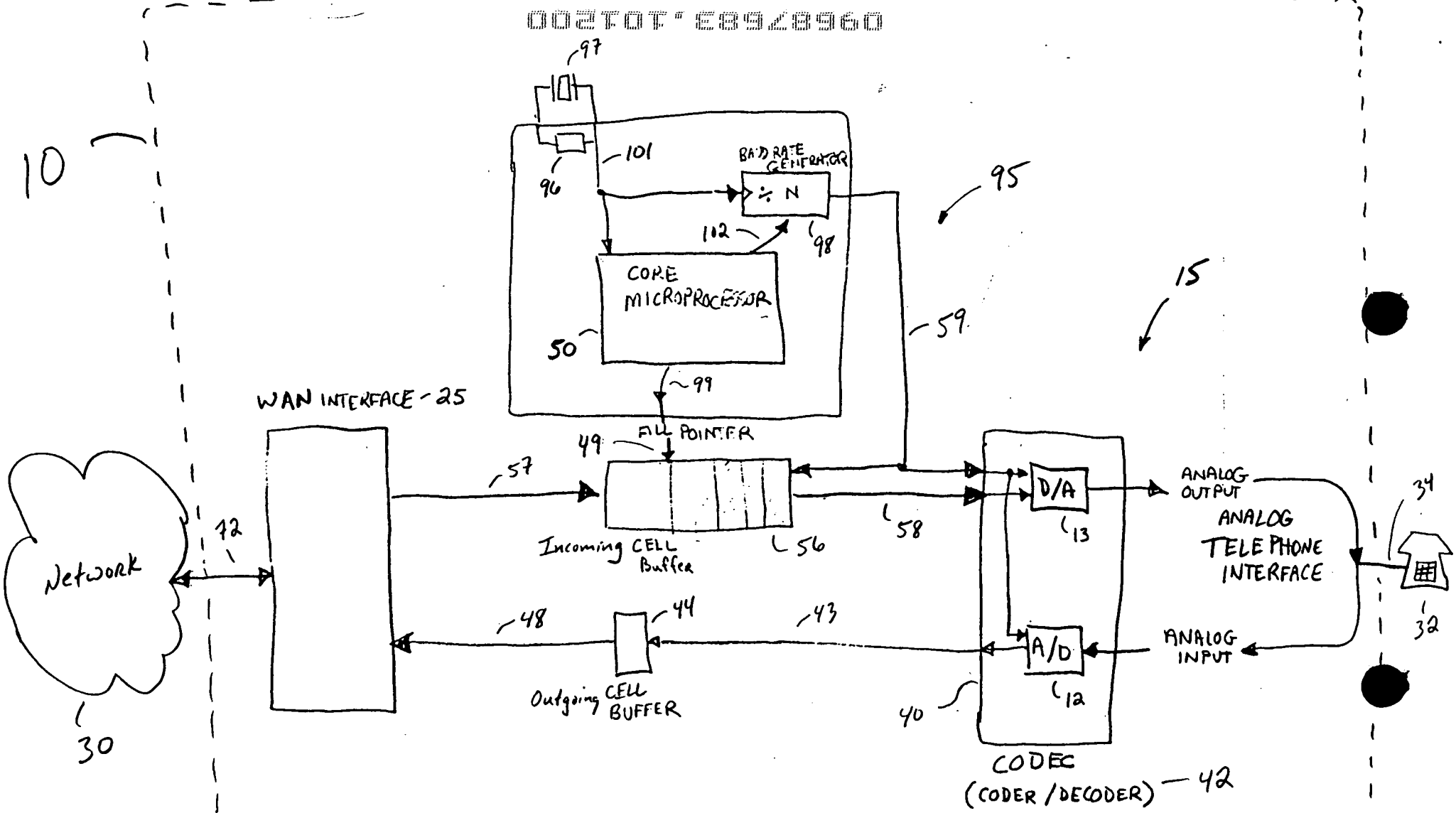
10
97
96
101
BAUD RATE GENERATOR
÷ N
98
95
112
CORE MICROPROCESSOR
50
59
15
99
WAN INTERFACE - 25
FILL POINTER
49
57
Network
12
30
Incoming CELL Buffer
56
58
D/A
13
ANALOG OUTPUT
ANALOG TELEPHONE INTERFACE
34
32
48
44
43
Outgoing CELL BUFFER
A/D
12
ANALOG INPUT
40
CODEC (CODER / DECODER)
42

Fig. 6